REMARKS

In the Official Action mailed on **29 June 2005**, the Examiner reviewed Claims 1-17. Claims 1-17 were rejected under 35 U.S.C. §102(b) as being anticipated by Santhanam (USPN 5,704,053, hereinafter "Santhanam").

Rejections under 35 U.S.C. §102(b)

Claims 1-17 were rejected as being anticipated by Santhanam. Applicant respectfully points out that Santhanam teaches inserting prefetch instructions into **loops in the code** (see Santhanam, col. 3, lines 47-55).

In contrast, the present invention inserts prefetch instructions into **preceding basic blocks** of the code, not into the same loop body (see page 17, lines 9-23 of the instant application). This is beneficial because inserting the prefetch instructions into preceding basic blocks allows more time for the data to be prefetched, thereby reducing the number of stalls waiting for memory access. There is nothing within Santhanam, either explicit or implicit, which suggests inserting prefetch instructions into preceding basic blocks of the code.

Accordingly, Applicant has amended independent claims 1, 8, and 13 to clarify that the present invention inserts prefetch instructions into preceding basic blocks of the code. These amendments find support on page 17, lines 9-23 of the instant application.

Hence, Applicant respectfully submits that independent claims 1, 8, and 13 as presently amended are in condition for allowance. Applicant also submits that claims 2-7, which depend upon claim 1, claims 9-12, which depend upon claim 8, and claims 14-17, which depend upon claim 13, are for the same reasons in condition for allowance and for reasons of the unique combinations recited in such claims.

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CONCLUSION

It is submitted that the present application is presently in form for allowance. Such action is respectfully requested.

Respectfully submitted,

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